



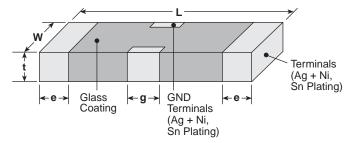
## three-terminal capacitor/resistor



### features

- · Improved reduction of radiated noises
- Capacitor/resistor filter
- Noise reduction in a variety of circuits
- Marking: Black body color with no marking
- Products with lead-free terminations meet EU RoHS requirements

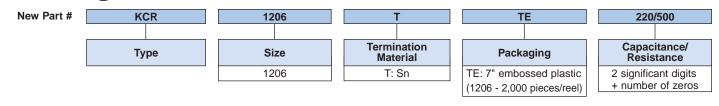
## dimensions and construction



Size	L	w	t	g	е
1206	.126±.008 (3.2±0.2)		.031±.008 (0.8±0.2)*		.016±.012 (0.4±0.3)

\* KCR1206T221/500: t = .043 ± .008 (1.1 ± 0.2) KCR1206T221/101: t = .043 ± .008 (1.1 ± 0.2)

## ordering information



For further information on packaging, please refer to Appendix A.

Specifications given herein may be changed at any time without prior notice. Please confirm technical specifications before you order and/or use.

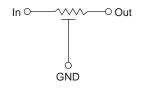
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# three-terminal capacitor/resistor

## circuit schematic



# applications and ratings

ltem	Specification
Operating Temperature Range	-40°C to +85°C
Storage Temperature Range	-40°C to +85°C
Measuring Condition (Standard) Temperature Relative Humidity	15°C to 35°C 20 - 90%
Measuring Condition (Precision) Temperature Relative Humidity	20°C ± 1°C 60 - 67%

## applications and ratings (continued)

Part Designation	Capacitance (pF)	Capacitance Tolerance	Resistance (Ω)	Resistance Tolerance (%)	Power Rating (W)	Operating Temperature Range
KCR1206TTE220/500	22		50			
KCR1206TTE220/101	22		100			
KCR1206TTE470/500	47	+50 ~ -20	50	±30	1/16	-40°C to +85°C
KCR1206TTE470/101	47		100			
KCR1206TTE101/500	100		50	±30	1/10	-40 C 10 +65 C
KCR1206TTE101/101	100		100			
KCR1206TTE221/500	220		50			
KCR1206TTE221/101	220		100			

# environmental applications

#### **Performance Characteristics**

Parameter	Requirement	Test Method
Insulation Resistance	1000 MΩ Minimum	Applied rated voltage for 60 seconds
Capacitance	Within the tolerance	Frequency: 1kHz Voltage: 1Vrms
DC Resistance	Within the tolerance	DC: 0.3V Maximum
Terminal Adhesion Strength	No physical damage	Solder a chip to a test substrate and then laterally apply a load (5N, 500gF) in the arrow direction
Resistance to Solder Heat	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	Flux: 25% rosin Preheating: 60 seconds Preheating Temperature: 150°C Solder: H60A Solder Temperature: 260°C ±5°C Dip Time: 5 seconds ± 0.5 second

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## three-terminal capacitor/resistor

## environmental applications (continued)

#### **Performance Characteristics**

Parameter	Requirement	Test Method
Solderability	More than 95% of the terminal electrode shall be covered with new solder	Flux: 25% rosin Preheating: 60 seconds Preheating Temperature: 150°C Solder: H60A Solder Temperature: 230°C ±5°C Dip Time: 4 seconds ± 1 second
Temperature Cycle*	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	Repeat the following heat cycle 10 times:Step:Temperature:Time:1 $-40^{\circ}C \pm 3^{\circ}C$ 30 minutes $\pm 3$ minutes2Room Temp.15 minutes maximum3 $85^{\circ}C \pm 2^{\circ}C$ 30 minutes $\pm 3$ minutes4Room Temp.15 minutes maximum
High Temperature Resistance*	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	Temperature: 70°C ± 2°C Bias: 150% of rated voltage Test Time: 1000 +48/-0 hours
Humidity Resistance (Unload)*	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	Temperature: $85^{\circ}C \pm 2^{\circ}C$ Humidity: $85\% \pm 5\%$ Test Time: 500 +24/-0 hours
Substrate Bending Test	Appearance: No physical damage Capacitance: Within tolerance	After soldering a chip to a test substrate, bend the substrate by 1 mm and then measure. The substrate is GE4 or based on GE4. Substrate $20$ Weight Displacement $45 \pm 2 - 45 \pm 2 - 4$
Humidity Resistance (Load)*	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	Temperature: 40°C ± 2°C Humidity: 90 - 95% Bias: 100% of rated voltage Test Time: 500 +24/-0 hours
Low Temperature Resistance (Unload)*	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	Temperature: -40°C ± 2°C Test Time: 1000 +48/-0 hours
Vibration	Appearance: No physical damage Capacitance: Within tolerance Dielectric Loss: Within tolerance Insulation Resistance: Within tolerance	The frequency of applied vibration should be swept from 10 Hz to 55 Hz and return to 10 Hz. This cycle time should be about 1 minute and this cycle should be repeated. Amplitude (Total Excursion): 1.5 mm This motion shall be applied for a period of 2 hours in each of 3 mutually perpendicular axes (total of 6 hours).

\* After temperature cycle test, high temperature resistance test, humidity resistance test or low temperature resistance test, the tested sample should be measured after having been left in temperature from 15°C to 35°C and relative humidity from 45% to 75% for 24 hours.

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**EMI/EMC** filtering

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